

ISSUE CLASSIFICATION	
Class	Subclass

<p><b>O.I.P.F.</b></p> <p>SCANNED <i>CC</i> Q.A. <i>ne</i></p>	<p><b>PATENT DATE</b></p>
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/576956	D	716	1	2768	Rossoshchik

# APPLICANTS

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# TITLE

Methodology for the design of high-performance communication architectures for system-on-chips using communication architecture tuners PTO-2040

PTO-2040  
12/89

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ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
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	_____ (Primary Examiner) (Date)	Amount Due	Date Paid		
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